ABSTRACT OF THE DISCLOSURE

An arithmetic operation method for a cyclic redundancy check is provided which is capable of performing a high-speed arithmetic operation for the cyclic redundancy check.

A cyclic redundancy check 32 arithmetic operation is performed on byte data making up output data using a 32nd order generative polynomial. A cyclic redundancy check 16 arithmetic operation is performed on byte data making up the output data using a 16th order generative polynomial. The cyclic redundancy check 16 arithmetic operation is performed on byte data making up the output data and on arithmetic operation result being obtained in a midpoint in the cyclic redundancy check 32 arithmetic operation using the 16th order generative polynomial.